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Docket No.: A-64873-1/MSS (463035-350)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

|                       |  |                 |                     |
|-----------------------|--|-----------------|---------------------|
| First Named Inventor: | JACK CHIHCHIEH YAO <i>et al.</i>   | Examiner:       | LUND,<br>Jeffrie R. |
| Appln. No.:           | 09/457,929   |                 |                     |
| Filing Date:          | December 8, 1999   | Group Art Unit: | 1763                |
|                       | Wafer Carrier and Semiconductor Apparatus for Processing a Semiconductor Substrate |                 |                     |
| Title:                | Semiconductor Substrate  |                 |                     |

PETITION FOR EXTENSION OF PERIOD FOR RESPONSE

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

Pursuant to 37 C.F.R. § 1.136(a), Applicant hereby petitions for an extension of 3-month(s) (from September 16, 2003 to December 16, 2003) within which to respond to the Office Action dated June 16, 2003. A check in the amount of \$475.00 (\$475.00 for 3 month extension fee for small entity) is enclosed herewith to cover the extension fee. Applicant is entitled to small entity fees as that the number of employees of the concern, including those of its affiliates, does not exceed 500 persons under 37 C.F.R. 1.27. Please charge any additional fees or credit any overpayment to our Deposit Account No. 50-2319 (Order No. A-64873-1/MSS (463035-350)).

12/24/2003 GWORDOF1 00000040 09457929

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475.00 OP

Respectfully submitted,  
DORSEY & WHITNEY LLP

Date: December 16, 2003

By: R. Michael Ananian  
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